

SKiiP® 3

2-pack-integrated intelligent Power System

SKiiP 2403 GB172-4DFL V3

Features

- SKiiP technology inside
- Trench IGBTs
- CAL HD diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- UL recognized File no. E63532

Typical Applications*

- Renewable energies
- Traction
- Elevators
- Industrial drives

Footnotes

¹ With assembly of suitable MKP capacitor per terminal

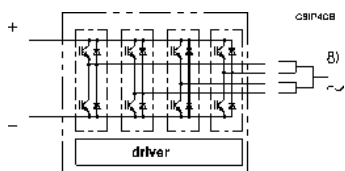
Absolute Maximum Ratings		$T_s = 25^\circ\text{C}$ unless otherwise specified	
Symbol	Conditions	Values	Unit
System			
$V_{CC}^{1)}$	Operating DC link voltage	1200	V
V_{isol}	DC, $t = 1$ s, main terminals to heat sink	5600	V
$I_{t(RMS)}$	per AC terminal, $T_{terminal} < 115^\circ\text{C}$	400	A
$I_{max (peak)}$	max. peak current of power section	2500	A
I_{FSM}	$T_j = 125^\circ\text{C}$, $t_p = 10$ ms, sin 180°	8000	A
I^2t	$T_j = 150^\circ\text{C}$, $t_p = 10$ ms, diode	320	kA^2s
f_{out}	fundamental output frequency	1	kHz
T_{stg}	storage temperature	-40 ... 85	$^\circ\text{C}$

IGBT			
V_{CES}	$T_j = 25^\circ\text{C}$	1700	V
I_C	$T_j = 150^\circ\text{C}$	$T_s = 25^\circ\text{C}$	2560
		$T_s = 70^\circ\text{C}$	1800
I_{Cnom}		2400	A
T_j	junction temperature	-40 ... 150	$^\circ\text{C}$

Diode			
V_{RRM}	$T_j = 25^\circ\text{C}$	1700	V
I_F	$T_j = 150^\circ\text{C}$	$T_s = 25^\circ\text{C}$	2059
		$T_s = 70^\circ\text{C}$	1400
I_{Fnom}		1800	A
T_j	junction temperature	-40 ... 150	$^\circ\text{C}$

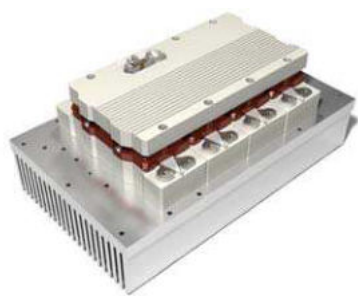
Driver			
V_s	power supply	13 ... 30	V
V_{iH}	input signal voltage (high)	$V_s + 0.3$	V
V_{isolPD}	QPD $\leq 10\text{pC}$, PRIM to POWER	1500	V
dv/dt	secondary to primary side	75	$\text{kV}/\mu\text{s}$
f_{sw}	switching frequency	7	kHz

Characteristics		$T_s = 25^\circ\text{C}$ unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 1200$ A at terminal	$T_j = 25^\circ\text{C}$	1.9	2.4	V
		$T_j = 125^\circ\text{C}$	2.2		V
V_{CE0}		$T_j = 25^\circ\text{C}$	1.00	1.20	V
		$T_j = 125^\circ\text{C}$	0.90	1.10	V
r_{CE}	at terminal	$T_j = 25^\circ\text{C}$	0.75	0.95	$\text{m}\Omega$
		$T_j = 125^\circ\text{C}$	1.1	1.3	$\text{m}\Omega$
$E_{on} + E_{off}$	$I_C = 1200$ A $T_j = 125^\circ\text{C}$	$V_{CC} = 900$ V	780		mJ
		$V_{CC} = 1200$ V	1150		mJ
$R_{th(j-s)}$	per IGBT switch			0.013	K/W
$R_{th(j-r)}$	per IGBT switch			0.008	K/W



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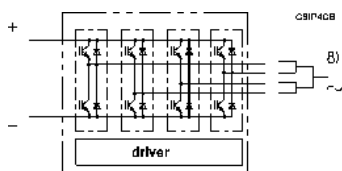
Typical Applications*

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Footnotes

¹ With assembly of suitable MKP capacitor per terminal

Characteristics			T _s = 25°C unless otherwise specified			
Symbol	Conditions		min.	typ.	max.	Unit
Diode						
V _F = V _{EC}	I _F = 1200 A at terminal	T _j = 25 °C		2.00	2.15	V
		T _j = 125 °C		1.80		V
V _{F0}		T _j = 25 °C		1.1	1.2	V
		T _j = 125 °C		0.8	0.9	V
r _F		T _j = 25 °C		0.74	0.8	mΩ
		T _j = 125 °C		0.8	0.9	mΩ
E _{rr}	I _F = 1200 A T _j = 125 °C	V _R = 900 V		144		mJ
		V _R = 1200 V		171		mJ
R _{th(j-s)}	per diode switch				0.025	K/W
R _{th(j-r)}	per diode switch				0.0246	K/W
Driver						
V _s	supply voltage non stabilized		13	24	30	V
I _{S0}	bias current @V _s =24V, f _{sw} = 0, I _{AC} = 0			330		mA
I _S	k ₁ = 58 mA/kHz, k ₂ = 0.00035 mA/A		= 330	+ k ₁ * f _{sw}	+ k ₂ * I _{AC}	mA
V _{IT+}	input threshold voltage (HIGH)		12.3			V
V _{IT-}	Input threshold voltage (LOW)				4.6	V
R _{IN}	input resistance			10		kΩ
C _{IN}	input capacitance			1		nF
t _{pRESET}	error memory reset time			0.0122		ms
t _{TD}	top / bottom switch interlock time			3		μs
t _{jitter}	jitter clock time			125		ns
t _{SIS}	short pulse suppression time			0.625	0.7	μs
I _{TRIPSC}	over current trip level		2450	2500	2550	A _{PEAK}
T _{trip}	over temperature trip level		110	115	120	°C
V _{DCtrip}	over voltage trip level,			not impl.		V
System						
t _{d(on)IO}	V _{CC} = 1200 V I _C = 1200 A T _j = 25 °C	turn on propagation delay time		1.4		μs
t _{d(off)IO}		turn off propagation delay time		1.4		μs
R _{th(r-a)}					0.0286	K/W
R _{CC'+EE'}	terminal to chip, T _s = 25 °C			0.13		mΩ
L _{CE}	commutation inductance			3		nH
C _{CHC}	per phase, AC-side			4		nF
I _{CES} + I _{RD}	V _{GE} = 0 V, V _{CE} = 1700 V, T _j = 25 °C			4.8		mA
M _{dc}	DC terminals, SI Units		6		8	Nm
M _{ac}	AC terminals, SI Units		13		15	Nm
w	SKiiP System w/o heat sink			3.1		kg
w _h	heat sink			8		kg



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SKiiP 2403 GB172-4DFL V3**Isolation coordination acc. to EN 50178 and IEC 61800-5-1**

Maximum grid RMS voltage, line-to-line, star point grounded mains	690V+20%
Installation altitude for maximum grid RMS voltage, line-to-line, star point grounded mains	2000m
Maximum transient peak voltage between low voltage circuit and mains	1600V
Pollution degree acc. to IEC 60664-1 outside the moulded power section	2
Overvoltage cat. acc. to IEC 60664-1 for mains	III
Basic isolation	between heat sink and mains; between low voltage circuit and mains
Protection level acc. to IEC 60529	IP00

Environmental conditions acc. to IEC 60721

	Storage	Transportation	Operation stationary use at weather protected locations	Operation - ground vehicle installations	Operation - ship environment
Climatic conditions	1K2	2K2	3K3 ₍₁₎	5K1	---
Biological conditions	1B1	2B1	3B1	5B1	6B1
Chemically active substances (excluded: salt spray)	1C2	2C1	3C2	5C2	6C2
Mechanically active substances	1S1	2S1	3S1	5S1	6S1
Mechanical conditions	1M3	(4)	3M6 ₍₂₎	5M3 ₍₃₎	6M3
Contaminating fluids	---	---	---	5F1	---

(1) 3K3 expanded temperature range: -40°C/+85°C

(2) 3M7 possible, but due to mechanic load capacity of external components like DC-Link capacitors limited to 3M6

(3) 5M3 without impact from foreign bodies, stones

(4) No declaration due to customer-specific packing

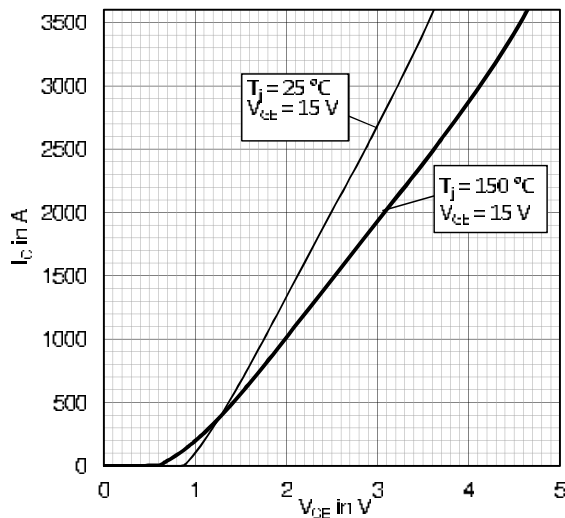


Fig. 1: Typical IGBT output characteristic

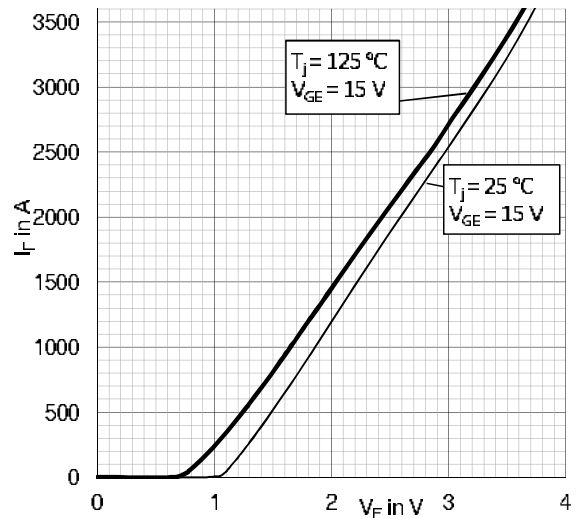


Fig. 2: Typical diode output characteristics

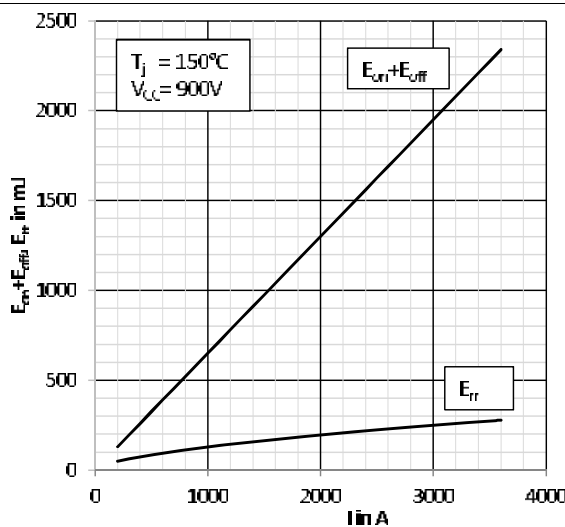


Fig. 3: Typical energy losses $E = f(I_C, V_{CC}, T_J)$

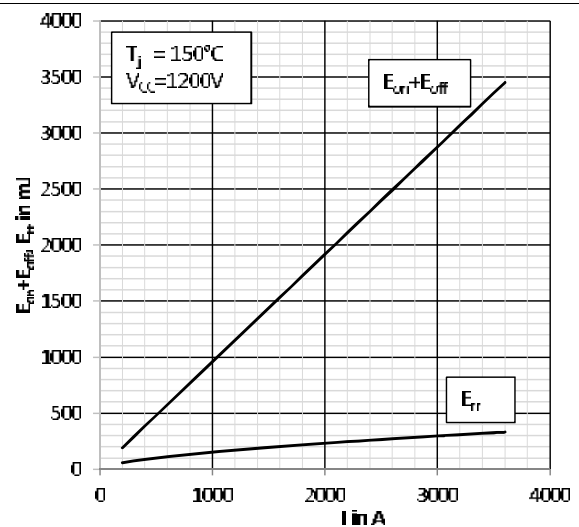


Fig. 4: Typical energy losses $E = f(I_C, V_{CC}, T_J)$

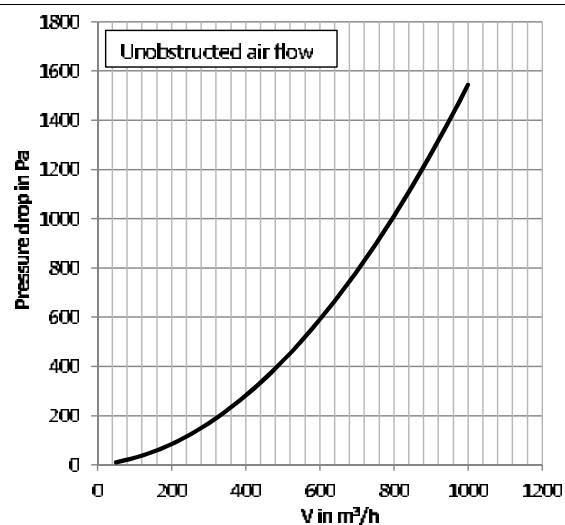


Fig. 5: Pressure drop Δp versus flow rate V

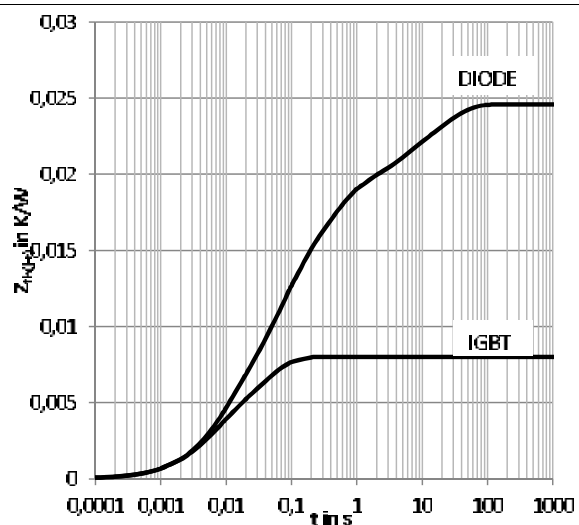
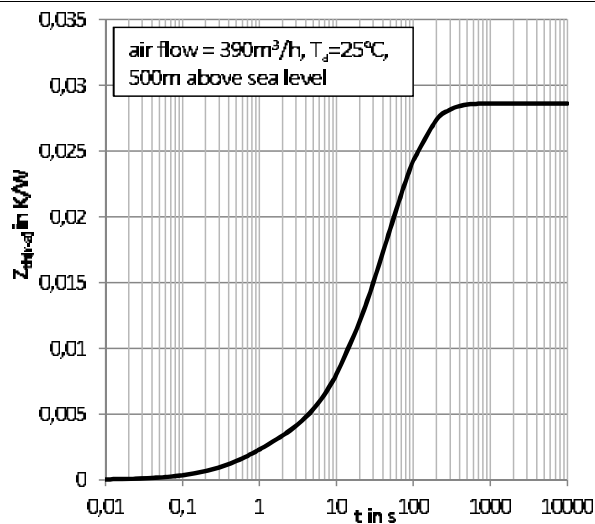


Fig. 6: Transient thermal impedance $Z_{th}(j-r)$


Fig. 7: Transient thermal impedance $Z_{th}(r-a)$

	$R_{th} [K/W]$				
	1	2	3	4	5
$Z_{th(j-r)} I$	0,0025	0,0055	0,0000	0,0000	0,0000
$Z_{th(j-r)} D$	0,0051	0,0012	0,0075	0,0033	0,0075
$Z_{th(j-r)} I$	0,0021	0,0033	0,0180	0,0052	0,0000
	$\tau [s]$				
	1	2	3	4	5
$Z_{th(j-r)} I$	0,0040	0,0460	1,0000	1,0000	1,0000
$Z_{th(j-r)} D$	0,0040	0,0160	0,0880	0,1300	1,300
$Z_{th(j-r)} I$	0,9000	20,000	100,000	170,00	1,0000

Fig. 8: Coefficients of thermal impedances

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX

* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our staff.